

Notice of References Cited	Application/Control No. 09/752,944	Applicant(s)/Patent Under Reexamination BURNS, GEOFFREY FRANCIS	
	Examiner John Manning	Art Unit 2614	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,502,512 A	03-1996	Toyoda et al.	348/706
	B	US-6,081,783 A	06-2000	Divine et al.	704/500
	C	US-6,630,964 B2	10-2003	Burns et al.	348/554
	D	US-5,666,169	09-1997	Ohki et al.	348/721
	E	US-6,484,220 B1	11-2002	Alvarez et al.	710/46
	F	US-6,542,203 B1	04-2003	Shadwell et al.	348/726
	G	US-2003/0072370 A1	04-2003	Girod et al.	375/240.13
	H	US-5,675,387	10-1997	Hoogenboom et al.	375/240.15
	I	US-5,471,592 A	11-1995	Gove et al.	709/213
	J	US-5,583,500 A	12-1996	Allen et al.	341/107
	K	US-5,646,687	07-1997	Botsford et al.	375/240.12
	L	US-5,685,005	11-1997	Garde et al.	712/36
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	R.C. Holland, Integrated Circuits and Microprocessors, 1986, Pergamon Books, Ltd., First edition, Pages 1-6
	V	K. Olukotun et al., The Case for a Single-Chip Multiprocessor, October 1996
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.